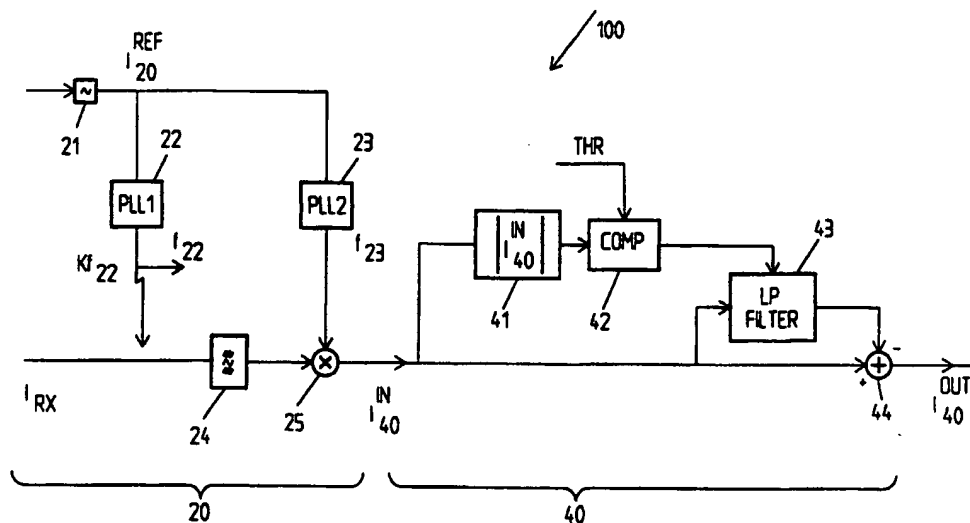




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(54) Title: ARRANGEMENT AND METHOD RELATING TO RADIO COMMUNICATION



(57) Abstract

The present invention relates to an arrangement (100) and a method respectively for suppressing clock overtones generated in a receiving arrangement of a radio communication system which receives signals within at least one frequency band and wherein the receiving arrangement comprises a number of oscillators including local oscillators and digital clocking means. The arrangement (100) comprises a phase locking arrangement (20) which includes a number of phase locking means (22; 23) for locking the oscillators in relation to each other so that all clock overtone frequencies become co-located and well defined within the frequency band(s). The arrangement further comprises a filtering arrangement (40) for filtering out said clock overtones, thus producing an output signal (I OUT/40) which is substantially free from clock overtones.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/SE 98/01935

A. CLASSIFICATION OF SUBJECT MATTER				
IPC6: H04B 1/10 According to International Patent Classification (IPC) or to both national classification and IPC				
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IPC6: H04B, H03H				
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C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
A	WO 9531860 A1 (ANALOG DEVICES, INC.), 23 November 1995 (23.11.95), page 5, line 16 - line 27, abstract. <div style="text-align: center;">-- -----</div>	1-22		
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.				
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12 May 1999		18 -05- 1999		
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INTERNATIONAL SEARCH REPORT

Information on patent family members

07/04/99

International application No.

PCT/SE 98/01935

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